

## Abstract of the Disclosure

Disclosed is a method for driving a liquid crystal display, in which the response speed of a liquid crystal is improved by the change of gate pulse voltage. The method comprises the steps of: sequentially generating a plurality of gate pulse voltages having 1st to 3rd levels while being synchronized with vertical clock signal in said 1 vertical period; in invert driving, dividing the generating period of the plural gate pulse voltages into a charge period, a holding period and a discharge period in respective polar periods corresponding to the 1st to 3rd levels of the plural gate pulse voltage; and converging pixel voltage of the discharge period to a common voltage level, wherein the 3rd level exists in a range between the 1st level and the 2<sup>nd</sup> level.